IN THE ABSTRACT:

Please amend the Abstract to read as follows:

B1

--A state control circuit gives an inactive state control signal to a CPU and an active state control signal to a data transmission circuit. In response to this, the CPU goes into the halt state and the data transmission circuit goes into the receive state. When receive processing is completed, the state control circuit gives an active state control signal to the CPU. In response to this, the CPU restores from the halt state to the operative state. The CPU gives an instruction signal to the state control circuit. The state control circuit gives an inactive state control signal to the data transmission circuit. In response to this, the data transmission circuit goes into the halt state.--

IN THE CLAIMS:

Please amend claim 1 as follows:

B2

1. (Amended) A contactless integrated circuit (IC) card that sends/receives data to/from the outside and is supplied with power from the outside in a contactless manner, comprising:

a transmission circuit for sending/receiving data to/from the outside;

a buffer memory;

a direct memory access (DMA) circuit for transmitting data received by said transmission circuit to said buffer memory and transmitting data stored in said buffer